



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani Art Unit : 1765
Serial No.: 09,596,755 Examiner : Robert Kunemund
Filed : June 15, 2000
Title : MANUFACTURING METHOD OF SEMICONDUCTOR AND MANUFACTURING
METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION


Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	47	-	47	=	\$0
Independent	7	-	7	=	\$0
Applicant hereby petitions under 37 C.F.R. §1.136 for a 2 month extension of time.					\$400
TOTAL FEE DUE					\$400

A check including \$400 is attached.

Respectfully submitted,

Date: 2/4/02


For Scott C. Harris
Reg. No. 32,930

JAMES T. HAGLER
REG. NO 40,631

Fish & Richardson P.C.
PTO Customer NO. 20985
4350 La Jolla Village Drive, Suite 500
San Diego, California 92122
Telephone: 619 594-1000
Facsimile: 619 594-1000

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being
deposited with the United States Postal Service as first class mail with
sufficient postage on the date indicated below and is addressed to the
Commissioner for Patents, Washington, D.C. 20231

James T. Hagler
Acting United States Commissioner for Patents